L Number	Hits	Search Text	DB	Time stamp
1	6569	partition$3 same rout$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/15 12:23
2	315	(partition$3 same rout$4) and diagonal and stor$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/15 12:24
3	315	((partition$3 same rout$4) and diagonal and stor$4) and rout$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/15 12:24
4	200	(((partition$3 same rout$4) and diagonal and stor$4) and rout$4) and region	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/15 12:25
5	165	((((partition$3 same rout$4) and diagonal and stor$4) and rout$4) and region) and (edge or shape)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/15 12:26
6	147	(((((partition$3 same rout$4) and diagonal and stor$4) and rout$4) and region) and (edge or shape)) and path	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/15 12:26
7	65	((((((partition$3 same rout$4) and diagonal and stor$4) and rout$4) and region) and (edge or shape)) and path) and 716/$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/15 12:27
8	36	(((((((partition$3 same rout$4) and diagonal and stor$4) and rout$4) and region) and (edge or shape)) and path) and 716/$.ccls.) and grid	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/15 12:27
9	70	(partition$3 same rout$4) same diagonal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/15 12:28
10	48	((partition$3 same rout$4) same diagonal) and rout$4 and (region or sub-region)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/15 12:28
11	46	(((partition$3 same rout$4) same diagonal) and rout$4 and (region or sub-region)) and (edge or shape)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/15 12:28

	Document ID	Issue Date	Pages	Title	Current OR
1	US 20030188286 A1	20031002	47	Method and Apparatus for Using a Diagona Line to Measure Congestion in a Region of an Integrated-Circuit Layout	716/13
2	US 20030132878 A1	20030717	57	Extended kalman filter for autonomous satellite navigation system	342/357.06
3	US 20030115566 A1	20030619	118	Method and apparatus for pre-computing routes	716/14
4	US 20030101428 A1	20030529	118	Routing method and apparatus	716/14
5	US 20030088845 A1	20030508	115	Probabilistic routing method and apparatus	716/14
6	US 20030088844 A1	20030508	118	Method and apparatus for pre-computing routes	716/14
7	US 20030088841 A1	20030508	50	Partitioning placement method and apparatus	716/8
8	US 20030079193 A1	20030424	118	Routing method and apparatus that use diagonal routes	716/7
9	US 20030066045 A1	20030403	118	Method and apparatus for identifying routes for nets	716/14
10	US 20030066044 A1	20030403	118	Method and apparatus for identifying propagation for routes with diagonal edges	716/14
11	US 20030066043 A1	20030403	117	Routing method and apparatus	716/13
12	US 20030066042 A1	20030403	118	ROUTING METHOD AND APPARATUS	716/13
13	US 20030064559 A1	20030403	118	Method and apparatus for generating routes for groups of related node configurations	438/218
14	US 20030063614 A1	20030403	118	Method and apparatus for storing routes for groups of related net configurations	370/401
15	US 20030063568 A1	20030403	118	Method and apparatus for storing routes	370/252

	Document ID	Issue Date	Pages	Title	Current OR
16	US 20030056187 A1	20030320	118	METHOD AND APPARTUS FOR ROUTING	716/14
17	US 20030043827 A1	20030306	118	LP method and apparatus for identifying route propagations	370/408
18	US 20030023943 A1	20030130	118	Method and apparatus for producing sub-optimal routes for a net by generating fake configurations	716/7
19	US 20030018947 A1	20030123	118	Hierarchical routing method and apparatus that use diagonal routes	716/7
20	US 20020199165 A1	20021226	118	Method and apparatus for pre-computing attributes of routes	716/14
21	US 20020174412 A1	20021121	118	Method and apparatus for pre-computing routes for multiple wiring models	716/12
22	US 20020170027 A1	20021114	56	Method and apparatus for pre-computing placement costs	716/10
23	US 20020166105 A1	20021107	118	"LP method and apparatus for identifying routes"	716/14
24	US 20020157075 A1	20021024	56	Method and apparatus for computing placement costs	716/10
25	US 20020147958 A1	20021010	117	Method and apparatus for adaptively selecting the wiring model for a design region	716/12
26	US 20020133798 A1	20020919	44	Method and apparatus for considering diagonal wiring in placement	716/10
27	US 20020073390 A1	20020613	44	Method and apparatus for using a diagonal line to measure an attribute of a bounding box of a net	716/8
28	US 20020069397 A1	20020606	56	Method and apparatus for placing circuit modules	716/12
29	US 6687893 B2	20040203	114	Method and apparatus for pre-computing routes for multiple wiring models	716/14

	Document ID	Issue Date	Pages	Title	Current OR
30	US 6678872 B2	20040113	44	Method and apparatus for using a diagonal line to measure congestion in a region of an integrated-circuit layout	716/7
31	US 6671864 B2	20031230	45	Method and apparatus for using a diagonal line to measure an attribute of a bounding box of a net	716/8
32	US 6618849 B2	20030909	113	Method and apparatus for identifying routes for nets	716/12
33	US 6608589 B1	20030819	54	Autonomous satellite navigation system	342/357.06
34	US 6516455 B1	20030204	48	Partitioning placement method using diagonal cutlines	716/7
35	NB83123895	19831201	2	Wiring Multinode Nets	
36	US 20030101428 A	20030529	118	Diagonal routing path designing method for integrated circuit layout, involves identifying route which connects subregions of integrated circuit region	
37	US 20030088845 A	20030508	115	Probabilistic routing method for integrated circuit layout design, involves identifying a route with diagonal edges, that connects subregions containing pins of net formed within layout region	
38	US 20030088844 A	20030508	118	Net routing method of integrated circuit layout for electronic design automation application, involves identifying route having partially diagonal route edge, which connects several sub-regions containing pins	

	Document ID	Issue Date	Pages	Title	Current OR
39	US 20030079193 A	20030424	118	Network routing method for integrated circuit layout, involves identifying partial diagonal route edge that connects set of sub-regions containing pins of network	
40	US 20030066042 A	20030403	118	Net routing method in integrated circuit layout, involves identifying route having diagonal route edge that connects set of sub-regions of IC containing pins of net	
41	US 20030056187 A	20030320	118	Routing method of net within particular region of integrated circuit, by identifying route connecting set of sub-regions containing pins of net, wherein route has route edge that is at least partially	
42	US 20030043827 A	20030306	118	Net routing method in integrated circuit designing, involves identifying specific route which has partially diagonal route edge and which connects set of sub-regions containing pins of net	
43	US 20020199165 A	20021226	118	Pin net routing method for designing integrated circuit layout, involves identifying diagonal routes for connecting pins of net	
44	US 20020174412 A	20040203	114	Net routing method for integrated circuit layout, involves identifying route having partially diagonal edges, that connects sub-regions including pins of net of integrated circuit layout	

	Document ID	Issue Date	Pages	Title	Current OR
45	US 20020166105 A	20021107	118	Net routing method for computer system, involves identifying route with partially diagonal route edge of sub-regions, containing pins of net in particular integrated circuit region	
46	US 20020147958 A	20020613	117	Net routing method in automated integrated circuit designing, involves identifying route with diagonal edges for connecting identified sub-regions containing net pins	